

Search Results -

Term	Documents
(22 AND 8).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	22
(L22 AND L8).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	22

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

L25

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Thursday, June 22, 2006 Printable Copy Create Case

<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
----------------------------	--

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L25 L22 and 18

22 L25

L24 L22 and 17

41 L24

L23 L22 and 16

76 L23

L22 predict\$5 near8 branch\$4 near25 cach\$3 near15 way\$1

101 L22

L21 predict\$5 near8 branch\$4 near25 cach\$3

1864 L21

DB=PGPB,USPT; PLUR=YES; OP=OR

L20 13 and 13

16 L20

L19 13 and 17

7 L19

L18 13 and 16

11 L18

L17 15 and 18

5 L17

L16 15 and 17

20 L16

<u>L15</u>	15 and 16	27	<u>L15</u>
<u>L14</u>	14 and 18	8	<u>L14</u>
<u>L13</u>	14 and 17	39	<u>L13</u>
<u>L12</u>	14 and 16	52	<u>L12</u>
<u>L11</u>	12 and 18	8	<u>L11</u>
<u>L10</u>	12 and 17	36	<u>L10</u>
<u>L9</u>	12 and 16	49	<u>L9</u>
<u>L8</u>	(711/130-153)[CCLS]	8194	<u>L8</u>
<u>L7</u>	(712/235,236,239,240)[CCLS]	888	<u>L7</u>
<u>L6</u>	(712/2-300)[CCLS]	12232	<u>L6</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L5</u>	L4 and (dependen\$5 or conflict\$4 or contend\$4 or contention\$1 or hazard\$4 or collaps\$5)	36	<u>L5</u>
<u>L4</u>	(section\$1 or region\$1 or area\$1 or bank\$1 or portion\$1 or segment\$1) near12 memor\$4 near55 predict\$5 near6 branch\$4	74	<u>L4</u>
<u>L3</u>	L2 and (conflict\$4 or contend\$4 or contention\$1 or hazard\$4 or collaps\$5)	17	<u>L3</u>
<u>L2</u>	(section\$1 or region\$1 or area\$1 or bank\$1 or portion\$1 or segment\$1) near12 memor\$4 near25 predict\$5 near6 branch\$4	71	<u>L2</u>
<u>L1</u>	bank\$1 near12 memor\$4 near25 predict\$5 near6 branch\$4	7	<u>L1</u>

END OF SEARCH HISTORY


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alt](#)

Welcome United States Patent and Trademark Office

Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "(((predict*) <near/15> branch* and (section*, region*, area*, portion*, bank*) <near/..."

Your search matched 8 of 1360403 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.☒ e-mail

» Search Options

[View Session History](#)[New Search](#)

Modify Search

(((predict*) <near/15> branch* and (section*, region*, area*, portion*, bank*) <nea

[Search](#)☐ Check to search only within this results set

Display Format:



Citation



Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

[view selected items](#)[Select All](#) [Deselect All](#)**1. One billion transistors, one uniprocessor, one chip**

Patt, Y.N.; Patel, S.J.; Evers, M.; Friendly, D.H.; Stark, J.;

[Computer](#)

Volume 30, Issue 9, Sept. 1997 Page(s):51 - 57

Digital Object Identifier 10.1109/2.612249

[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(328 KB) IEEE JNL[Rights and Permissions](#)**2. An architectural framework for runtime optimization**

Merten, M.C.; Trick, A.R.; Barnes, R.D.; Nystrom, E.M.; George, C.N.; Gyllenhaal, J.C.; Hwu, W.-M

[Computers, IEEE Transactions on](#)

Volume 50, Issue 6, June 2001 Page(s):567 - 589

Digital Object Identifier 10.1109/12.931894

[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(3812 KB) IEEE JNL[Rights and Permissions](#)**3. Software Trace Cache**

Ramirez, A.; Larriba-Pey, J.L.; Valero, M.;

[Computers, IEEE Transactions on](#)

Volume 54, Issue 1, Jan 2005 Page(s):22 - 35

Digital Object Identifier 10.1109/TC.2005.13

[AbstractPlus](#) | Full Text: [PDF](#)(1032 KB) IEEE JNL[Rights and Permissions](#)**4. Deep jam: conversion of coarse-grain parallelism to instruction-level and vector parallelism applications**

Carribault, P.; Cohen, A.; Jalby, W.;

[Parallel Architectures and Compilation Techniques, 2005. PACT 2005. 14th International Conferen](#)

17-21 Sept. 2005 Page(s):291 - 300

Digital Object Identifier 10.1109/PACT.2005.16

[AbstractPlus](#) | Full Text: [PDF](#)(264 KB) IEEE CNF[Rights and Permissions](#)**5. Coherent Network Interfaces for Fine-Grain Communication**

Hill, M.D.; Falsafi, B.; Wood, D.A.; Mukherjee, S.S.;

[Computer Architecture, 1996 23rd Annual International Symposium on](#)

22-24 May 1996 Page(s):247 - 247

Digital Object Identifier 10.1109/ISCA.1996.10009

[AbstractPlus](#) | Full Text: [PDF](#)(1304 KB) | [IEEE CNF](#)
[Rights and Permissions](#)



6. MGS: A Multigrain Shared Memory System

Kubiatowicz, J.; Agarwal, A.; Yeung, D.;

[Computer Architecture, 1996 23rd Annual International Symposium on](#)

22-24 May 1996 Page(s):44 - 44

Digital Object Identifier 10.1109/ISCA.1996.10004

[AbstractPlus](#) | Full Text: [PDF](#)(1064 KB) | [IEEE CNF](#)
[Rights and Permissions](#)



7. Proceedings the Ninth International Symposium on High-Performance Computer Architectu

[High-Performance Computer Architecture, 2003. HPCA-9 2003. Proceedings. The Ninth Internatio](#)

8-12 Feb. 2003

Digital Object Identifier 10.1109/HPCA.2003.1183518

[AbstractPlus](#) | Full Text: [PDF](#)(237 KB) | [IEEE CNF](#)
[Rights and Permissions](#)



8. Access region locality for high-bandwidth processor memory system design

Sangyeun Cho; Pen-Chung Yew; Gyungho Lee;

[Microarchitecture, 1999. MICRO-32. Proceedings. 32nd Annual International Symposium on](#)

16-18 Nov. 1999 Page(s):136 - 146

Digital Object Identifier 10.1109/MICRO.1999.809451

[AbstractPlus](#) | Full Text: [PDF](#)(592 KB) | [IEEE CNF](#)
[Rights and Permissions](#)

[Help](#) [Contact Us](#) [Privacy](#)

© Copyright 2006 IEEE

